



Academic Personnel Short Profile / Short CV

University:	University of Nicosia
Surname:	Neophytou
Name:	Stelios
Rank/Position:	Associate Professor/Head of Department
Faculty:	Sciences and Engineering
Department:	Engineering
Scientific Domain: *	Computer Engineering – Computer Hardware

** Field of Specialization*

Academic qualifications (list by highest qualification)

Qualification	Year	Awarding Institution	Department	Thesis title (Optional Entry)
PhD	2009	University of Cyprus, Cyprus	Electrical and Computer Engineering	High Quality Test Pattern Generation Techniques for Digital VLSI Circuits
Engineering Diploma	2003	University of Patras, Greece	Computer Engineering and Informatics	Quality of Service in Wireless Local Networks (WLANS)

Employment history in Academic Institutions/Research Centers – List by the three (3) most recent

Period of employment		Employer	Location	Position
From	To			
2019	Present	University of Nicosia	Nicosia, Cyprus	Associate Professor
2013	2019	University of Nicosia	Nicosia, Cyprus	Assistant Professor
2009	2013	University of Nicosia	Nicosia, Cyprus	Lecturer
2008	2009	University of Cyprus	Nicosia, Cyprus	Special Teaching Staff

Key refereed journal papers, monographs, books, conference publications etc. List the five (5) more recent and other five (5) selected –(max total 10)

Ref. Number	Year	Title	Other authors	Journal and Publisher / Conference	Vol.	Pages
1	2020	Modelling Cross-National Differences in Automated Vehicle Acceptance	S. Etzioni, J. Hamadneh, A. B. Elvarsson, D. Esztergár-Kiss, M. Djukanovic, J. Sodnik, A. Polydoropoulou, I. Tsouros, C. Pronello, N. Thomopoulos, Y. Shiftan	MDPI Sustainability Journal	Vol.: 12 Issue: 22	9765-9789
2	2019	Exploiting Shared-Memory to Steer Scalability of Fault Simulation using Multicore Systems	S. Hadjitheophanous, M. K. Michael	IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems	Vol.: 28 Issue: 2	553-564
3	2018	Path Representation in Circuit Netlists Using Linear-Sized ZDDs with Optimal Variable Ordering	M. K. Michael	Journal of Electronic Testing	Vol. 34, Issue 6	667–683
4	2018	Dependability Threads	C. Bolchini, M. K. Michael, A. Miele	Book chapter in M. Ottavi, S. Pontarelli, D. Gizopoulos, "Dependable Multicore Architectures at Nanoscale"	1st Ed., ISBN-13: 9783319544212.	37-92
5	2016	Utilizing Shared Memory Multi-cores to Speed-up the ATPG process	S. Hadjitheophanous, M. K. Michael	European Test Symposium	21 st Conference,	1-6
6	2014	Multiple detection test generation with diversified fault partitioning paths	M. K. Michael	Microprocessors and Microsystems Journal	Vol. 38, Issue 6	585–597

7	2012	Test Pattern Generation of Relaxed n -Detect Test Sets	M. K. Michael	IEEE Transactions on Very Large Scale Integration (VLSI) Systems	Vol.: 20, Issue: 3, ISSN: 1063-8210	410 - 423
8	2012	A non-enumerative technique for measuring path correlation in digital circuits	M. K. Michael	Journal of Electronic Testing	Vol. 28, Issue 6, ISSN: 0923-8174	843–856
9	2011	An Approach for Quantifying Path Correlation in Digital Circuits without any Path or Segment Enumeration	M. K. Michael	European Test Symposium	16 th Conference ISBN:	141-146
10	2010	Test Set Generation with a Large Number of Unspecified Bits Using Static and Dynamic Techniques	M. K. Michael	IEEE Transactions on Computers	vol. 59, no. 3,	301-316

Research Projects. List the five (5) more recent and other five (5) selected (max total 10)				
Ref. Number	Date	Title	Funded by	Project Role*
1	2020	Maritime Cognitive Decision Support System	Cyprus Research Promotion Foundation (IPE)	CO-Primary Investigator
2	2012	“Automated Sonar-Thermal Monitoring for Sea Border Security”	Cyprus Research Promotion Foundation (IPE)	Research Team Member
3	2006	Novel Methods for Fault Tolerant Systems-on-Chip (SoCs)	Intel Corp	Research Team Member
4	2004	Delay Testing for VLSI Circuits	Cyprus Research Promotion Foundation (IPE)	Researcher

*Project Role: i.e. Scientific/Project Coordinator, Research Team Member, Researcher, Assistant Researcher, other

**Academic Consulting Services and/or Participation in Councils / Boards/ Editorial Committees.
List the five (5) more recent (Optional Entry)**

Ref. Number	Period	Organization	Title of Position or Service	Key Activities
1	12/16-	European Test Symposium (ETS)	Member of Technical Program Committee	Review of conference papers and technical support of the conference.
2	12/17-	COST action CA16222 "Wider Impacts and Scenario Evaluation of Autonomous and Connected Transport"	Member of Management Committee	Meetings and publication preparation.
3	12/11-11/15	COST action IC1103 "MEDIAN: Manufacturable and Dependable Multicore Architectures at Nanoscale"	Member of Management Committee	Meetings and publication preparation.

**Awards / International Recognition (where applicable). List the five (5) more recent and other five (5) selected.
(max total 10) (Optional Entry)**

Ref. Number	Date	Title	Awarded by:
1	2019	Senior Member Status	Institute of Electrical and Electronics Engineers.
2	2003	Scholarship for Graduate studies.	Cyprus Government Scholarship Foundation (IKY-Cyprus)
3	2003	Award for his overall academic performance.	Greek Technical Chamber (TEE)
4	2003	Graduate first among his class (distinction).	University of Patras
5	2001	Scholarship for academic performance (ranked 2 nd among his class.	Greek Government Scholarship Foundation (IKY-Hellas)