UNIVERSITY of NICOSIA

Course Code	Course Title	ECTS Credits
ECE-525	Computer Aided Design for VLSI	8
Department	**Semester**	**Prerequisites**
Engineering	Fall or Spring	ECE-111, ECE-220
Type of Course	**Field**	**Language of Instruction**
Elective	Engineering	English
Level of Course	**Year of Study**	**Lecturer(s)**
2nd Cycle	1st	Dr Stelios Neophytou
Mode of Delivery	**Work Placement**	**Co-requisites**
Face-to-face	N/A	None

Objectives of the Course:

The main objectives of this course are to:

- Provide the main principles of modern VLSI circuit design using computer tools.
- Present tool families and familiarize with popular design tools.
- Describe the basic algorithms used for modeling, design synthesis, simulation and analysis of ICs.

Learning Outcomes:

After completion of the course students are expected to be able to:

- Demonstrate the operation of the modern VLSI design/verification tools categories and its usage in integrated circuit fabrication.
- Use CAD tools to design and/or simulate a design of an integrated circuit.
- Identify the different algorithms used for automation (synthesis, simulation, floor planning, and routing) of modern VLSI circuits.
- Discuss the main approaches followed for the verification and manufacturing testing of VLSI circuits.
- Design computer based tools for helping the design process of VLSI circuits.

Course Contents:

- The Characteristics of Digital Electronic Design.
- Design Environments (system level, algorithm level, component level, layout level)
- Hierarchy and view representation. Connectivity and geometry representation.
- Synthesis tools for two level logic and hardware description languages.
- Static analysis tools including design rule checking and electrical rule checkers.
- Dynamic analysis tools for circuit level and logic level simulation.
- Functional and behavioral analysis. Event driven simulation.
- Programmable logic. Filed programmable gate arrays (FPGAs) and Complex programmable logic devices (CPLDs).
- Manufacturing process and overview of the manufacturing cycles. Describe the steps and the CAD tools used at each step.
- Design Verification and manufacturing testing.
- Design for reliability and manufacturability.

Learning Activities and Teaching Methods:

Lectures, Project. Homework Assignments. Research literature review and presentation.

Assessment Methods:

Homework, Mid-Term, Project, Final Exam, Presentation.

Required Textbooks/Reading:

Authors	Title	Publisher	Year	ISBN
Laung-Terng Wang, Yao-Wen Chang & Kwang-Ting (Tim) Cheng	Electronic Design Automation. Synthesis, Verification, and Test	Morgan Kaufmann	2009	978-0-12-374364-0

Recommended Textbooks/Reading:

Authors	Title	Publisher	Year	ISBN
J. M. Rabaey, A P. Chandrakasan, B. Nikolic	Digital Integrated Circuits, 2nd Edition	Prentice-Hall	2003	
N. A. Sherwani	Algorithms for VLSI Physical Design Automation, 3rd Edition	Springer	1999	
S. M. Sait, H. Youssef	VLSI Physical Design Automation: Theory and Practice	World Scientific Publishing Company	1999	
G. De Micheli	Synthesis and Optimization of Digital Circuits	Mac-Graw Hill	1994	